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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/600,757	06/23/2003	Byeong Koo Kim	8733.842.00	5730
30827	7590	06/01/2005	EXAMINER	
MCKENNA LONG & ALDRIDGE LLP 1900 K STREET, NW WASHINGTON, DC 20006			VU, PHU	
			ART UNIT	PAPER NUMBER
			2871	

DATE MAILED: 06/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

DETAILED ACTION

Response to Arguments

Claims 11-12 and 28-29 have been amended to overcome the previous objections due to minor informalities, therefore the objection of claims 11-12 and 28-29 due to minor informalities is withdrawn.

Applicant's arguments filed 3/7/2005 have been fully considered but they are not persuasive.

Regarding claims 1 and 18, applicant argues "Wu does not disclose 'a static electricity prevention circuit having a thin film transistor.' " However examiner cited the Wu's abstract which clearly states "A CCFET is formed as a TFT transistor and typically has a floating gate." The CCFET (referred to as element 50 in the rejection of claims 1 and 18) was cited as a part of the static electricity prevention circuit, and formation as a TFT would indicate that it is a TFT. The newly added limitation of the equi-potential line is disposed at the outer portion from the signal pad part. The reference shows an equipotential line (fig. 9 element 130) disposed at an outer portion from the signal gate pad. The specification nor the claim is indicative of what the outer portion does and does not consist of therefore this is considered to be outside the pixel electrode. Element 130 is connected to the signal gate pads through element 50 therefore the limitation from the gate pads is also met. A new rejection will also be written to reject the amended claims.

Claim Rejections - 35 USC § 102

Claims 1 and 18 are rejected under 35 U.S.C. 102(e) as being anticipated by Wu et. al US Patent No. 6175394.

Regarding claims 1 and 18, Wu discloses a liquid crystal display for preventing static electricity comprising: a signal pad part for applying a driving signal to be inputted to the signal lines of the picture display part (see figure 9), wherein the signal pad part includes: a plurality of pads connected to respective ones of the signal lines (see figure 9 element 112 and 114), and a static electricity prevention circuit having a TFT (figure 9 element 50) with a floating gate (see abstract) for connecting at least one of the said pads with an equipotential line (see element 130 "shorting ring" or "guarding ring") in the presence of static electricity. The reference does not disclose a plurality of liquid crystal cells but all liquid crystal displays will have a plurality of liquid crystal cells therefore this limitation is inherent to the reference. The reference shows an equipotential line (fig. 9 element 130) disposed at an outer portion from the signal gate pad. The specification nor the claim is indicative of what the outer portion does and does not consist of therefore this is considered to be outside the pixel electrode. Element 130 is connected to the signal gate pads through element 50 therefore the limitation from the gate pads is also met. A new rejection will also be written to reject the amended claims. Regarding claim 18, this claim mirrors claim 1 in method form but adds not steps that would not be anticipated by the device of claim 1 therefore the reference meets all the limitations of claim 18.

Claims 2-7, 11, 13, 15-17, 19-24 and 28-34 stand rejected under 35 U.S.C. 102(e) as the new rejection applied relies on the same embodiment of the same prior art.

Allowable Subject Matter

Claims 8-10, 12, 14, 25-27, and 29 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Reasons for allowance has been indicated in the previous office action and remain unchanged.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

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the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

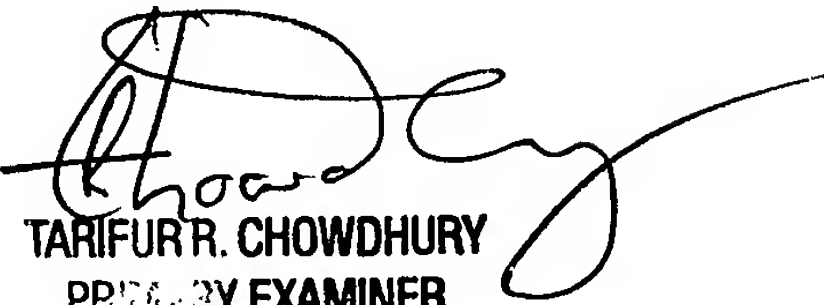
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phu Vu whose telephone number is (571)-272-1562.

The examiner can normally be reached on 8AM-5PM M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571)-272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Phu Vu
Examiner
AU 2871


TARIFUR R. CHOWDHURY
PRIMARY EXAMINER